

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Michael Hergott

Serial No. :

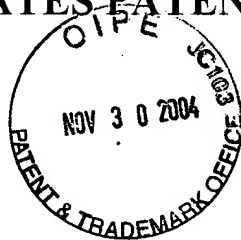
09/821,226

Filed :

March 28, 2001

For :

JTAG Test Access Port Controller
Used to Control Input/Output Pad
Functionality



Group Art Unit :

2133

Examiner :

Dooley, Matthew C.

Atty Docket :

1496.00095 / 00-555

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

11/30/04 *Connie del Castillo*
Date Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331

Date: 11/27/04